

FIG. 1

APPROVED	O.F. FIG.
BY	Cons. B. S.
DRAFTSMAN	

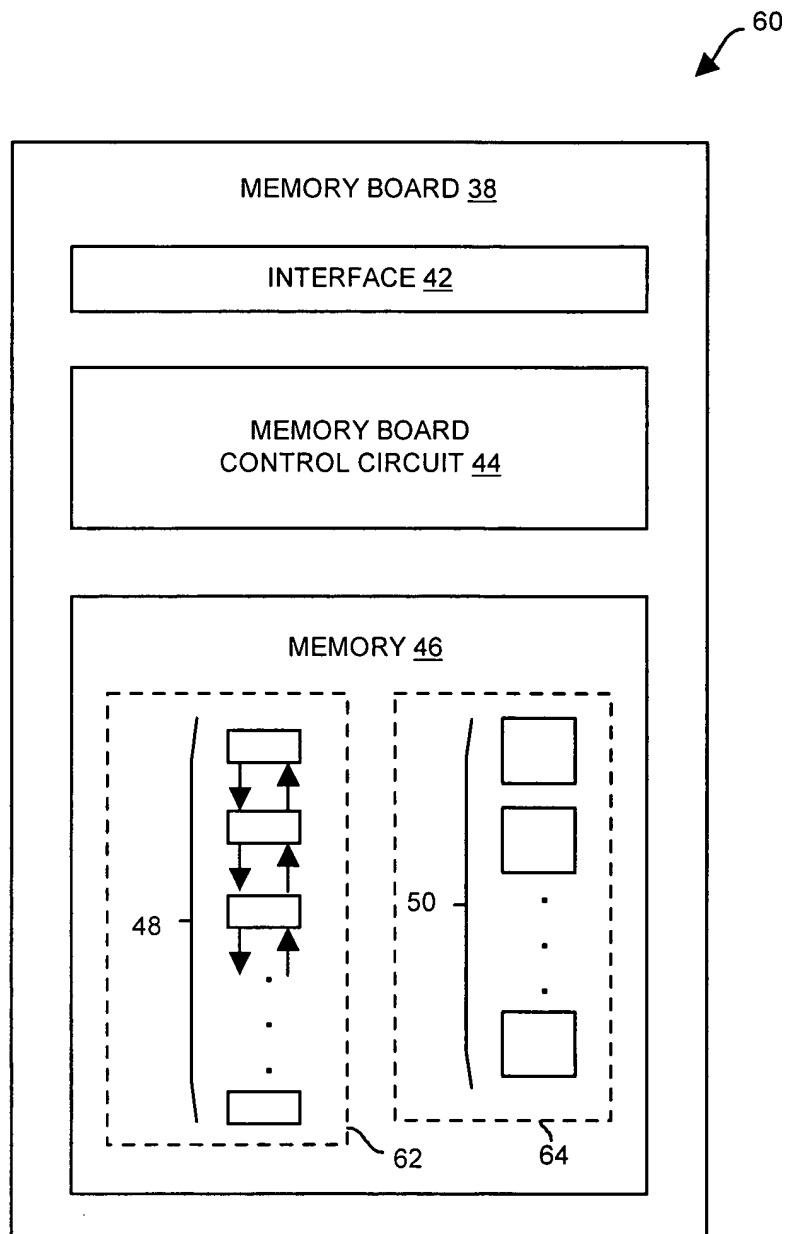


FIG. 2

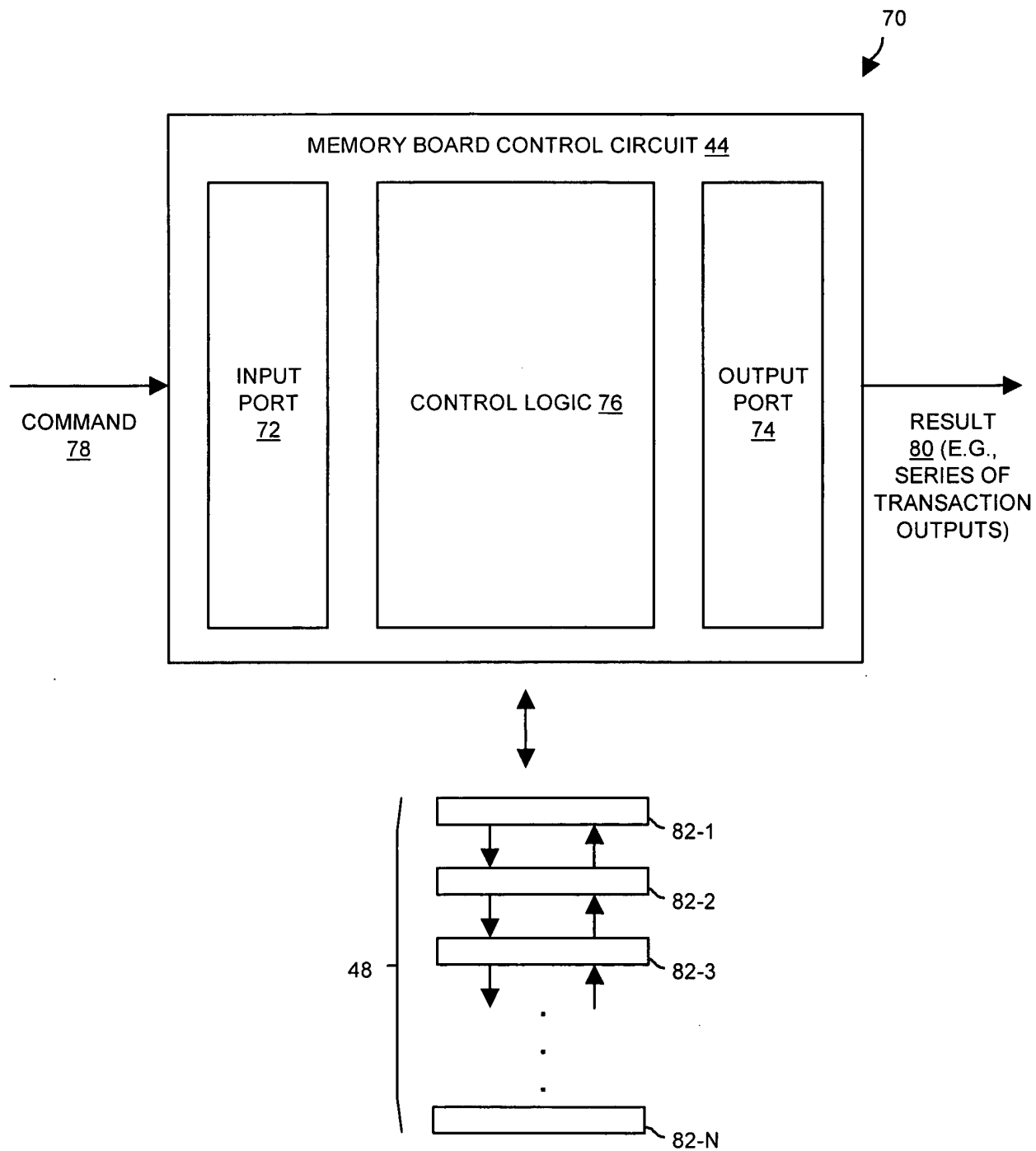


FIG. 3

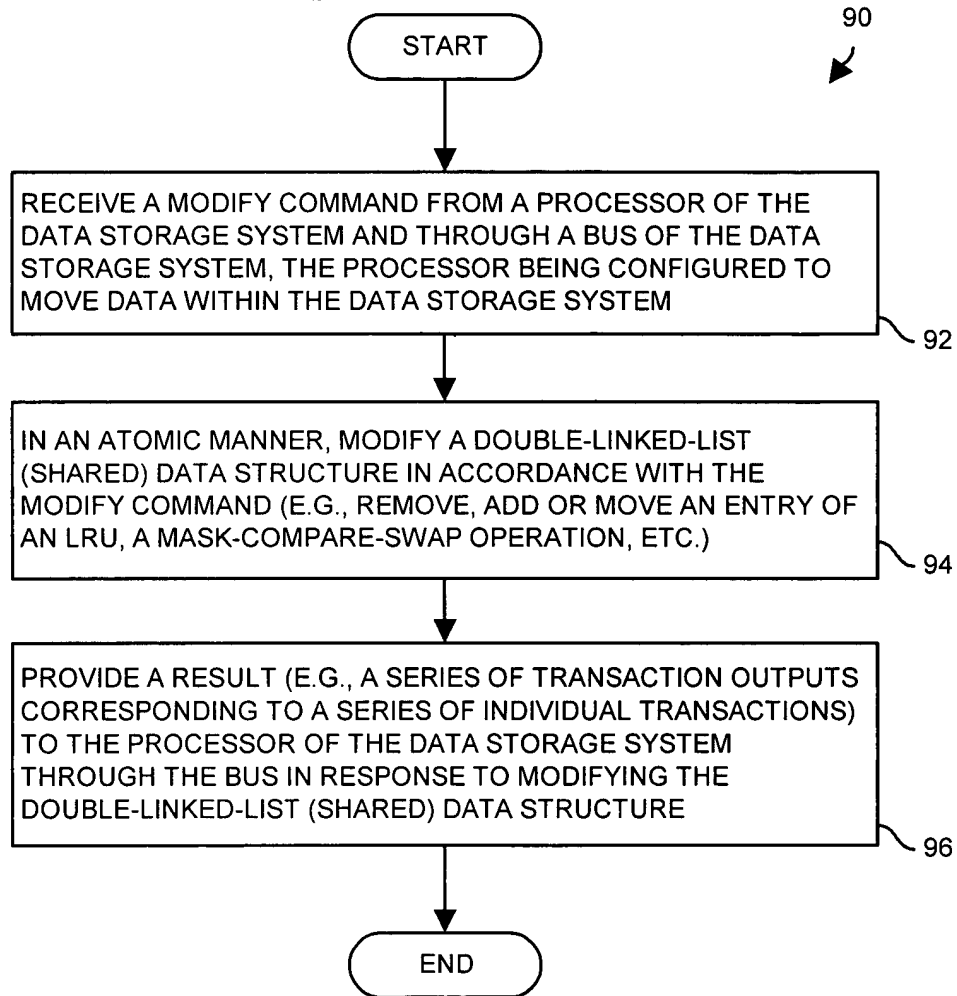


FIG. 4

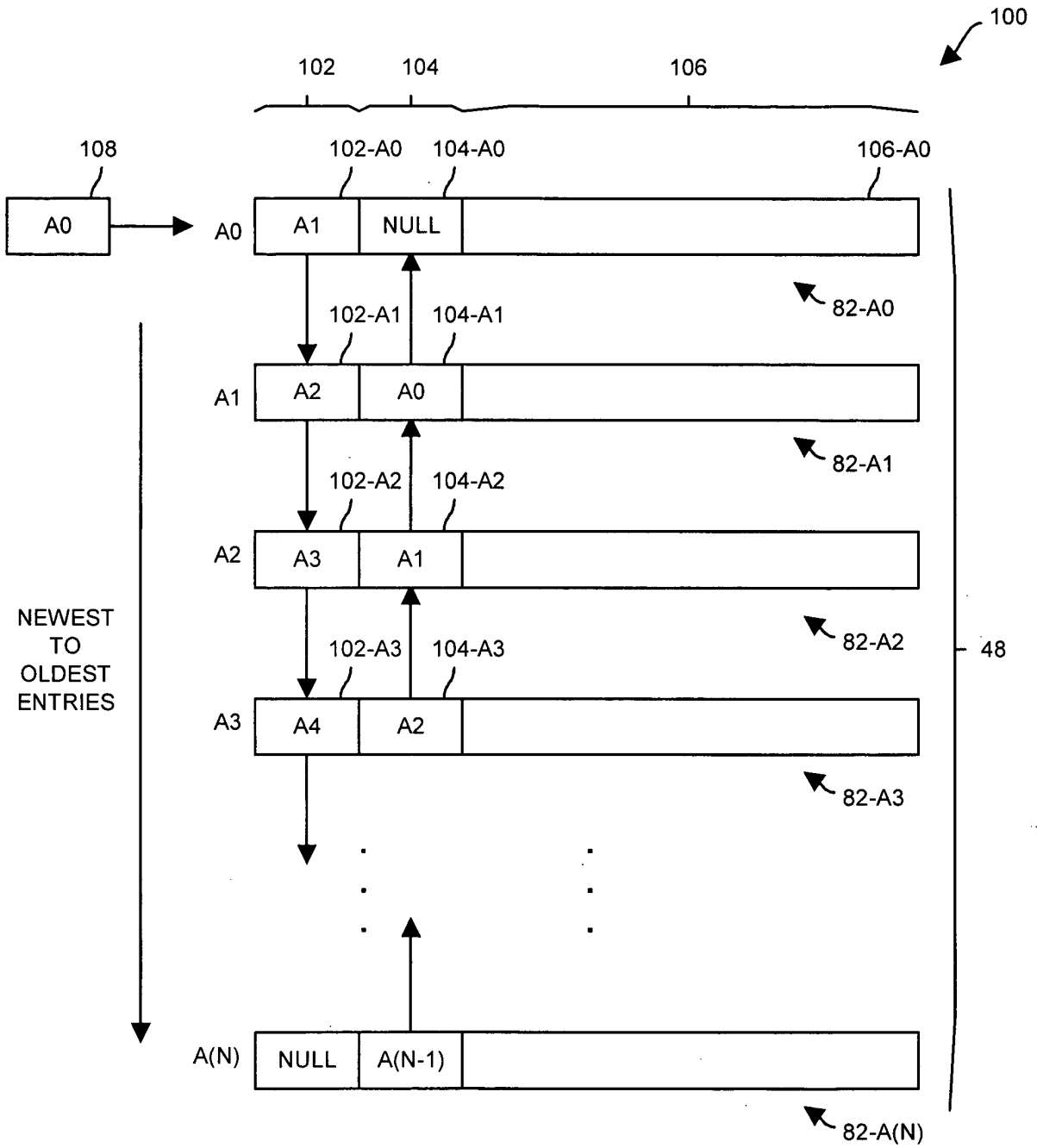


FIG. 5

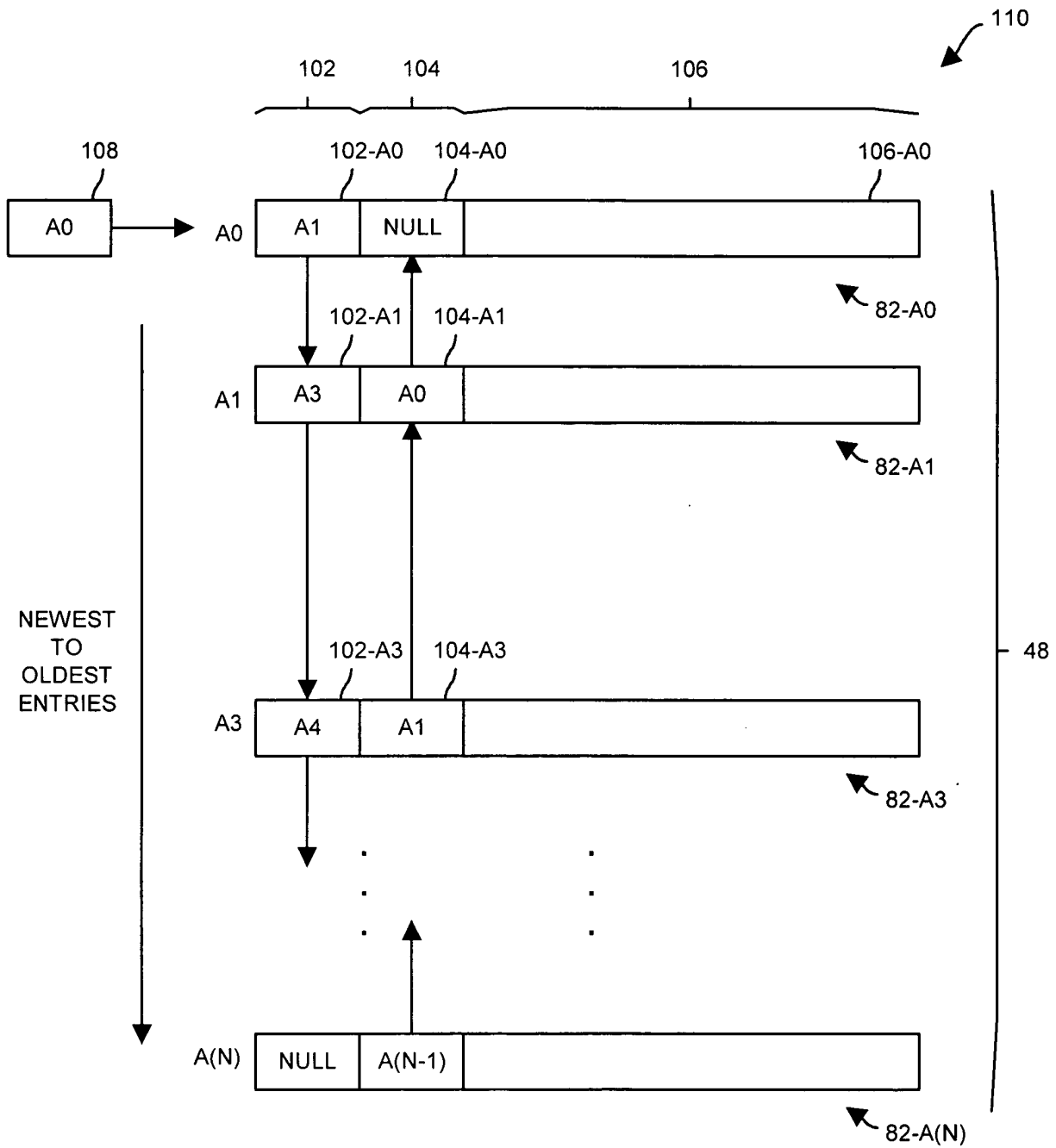


FIG. 6

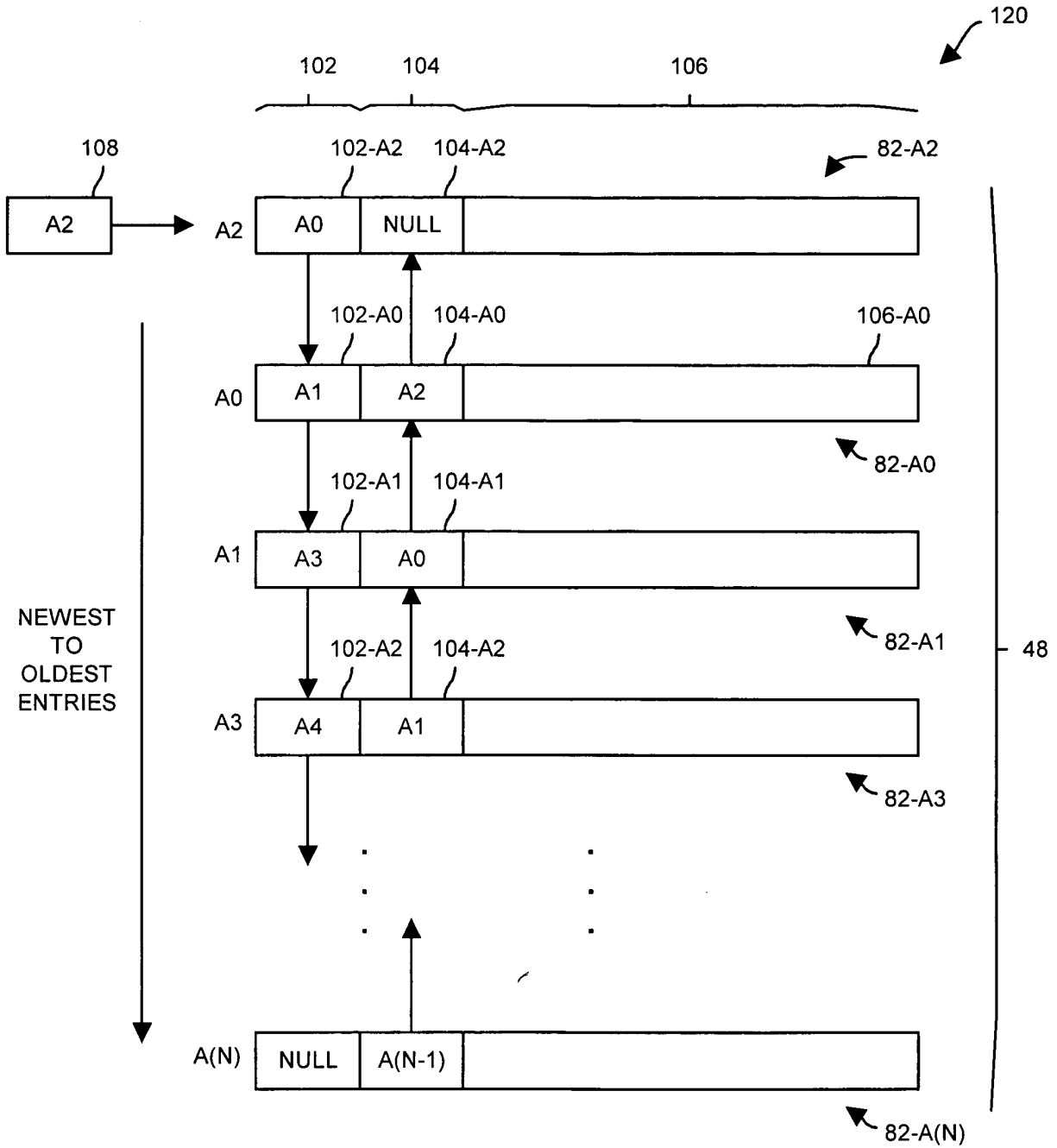


FIG. 7

APPROVED	FIG.
BY	DATE
DRAWN	

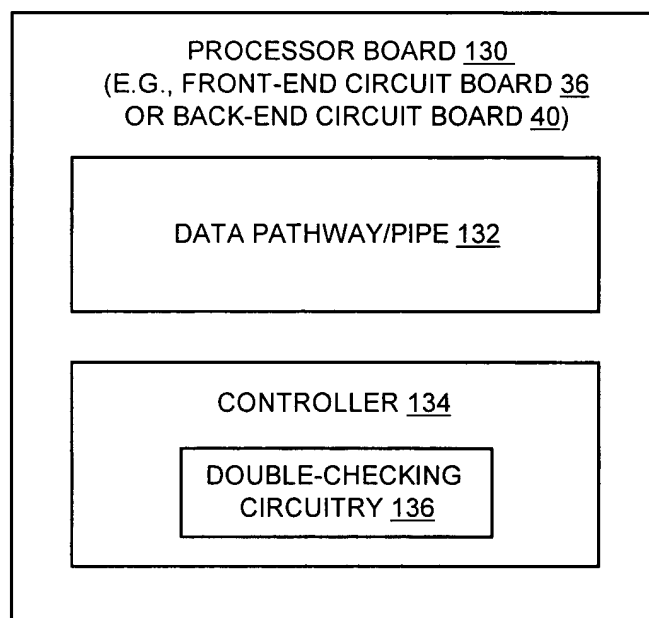


FIG. 8

PROCESSOR
BOARD 130
(E.G., FRONT-END
DIRECTOR)

ONE OF THE SET OF BUSES 32

CONTROL LOGIC 76
OF MEMORY
BOARD 38

COMMAND 78

TRANSACTION
OUTPUT 142-1

TRANSACTION
OUTPUT 142-2

TRANSACTION
OUTPUT 142-3

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TRANSACTION
OUTPUT 142-X

ERROR
MESSAGE 148-1

SERIES OF
INDIVIDUAL
TRANSACTIONS 144
(E.G., ONE OR MORE
MASK-COMPARE-SWAP
OPERATIONS)

SERIES OF
GENERATE
AND COMPARE
OPERATIONS 146
(E.G., GENERATE
PSEUDO
TRANSACTION
OUTPUT
AND
COMPARE
TO ACTUAL
TRANSACTION
OUTPUT)

PERFORM
ERROR
HANDLING
ROUTINE 150

FIG. 9